

# **Notice of Allowability**

Application No.

10/670,762

Examiner

Jean B. Jeanglaude

Applicant(s)

PAPPALARDO ET AL.

Art Unit

2819

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

- ☒ This communication is responsive to amendment filed on 02-03-05.
- ☒ The allowed claim(s) is/are 1-43.
- ☒ The drawings filed on 25 September 2003 are accepted by the Examiner.
- ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
  - ☒ All b) ☐ Some\* c) ☐ None of the:
    - ☒ Certified copies of the priority documents have been received.
    - ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
    - ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

\* Certified copies not received: \_\_\_\_\_.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.  
**THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.**

- ☐ A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
  - ☐ CORRECTED DRAWINGS (as "replacement sheets") must be submitted.
    - ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached
      - ☐ hereto or 2) ☐ to Paper No./Mail Date \_\_\_\_\_.
    - ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date \_\_\_\_\_.
- Identifying Indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
- ☐ DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

## **Attachment(s)**

- ☒ Notice of References Cited (PTO-892)
- ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- ☐ Information Disclosure Statements (PTO-1449 or PTO/SB/08), Paper No./Mail Date \_\_\_\_\_
- ☐ Examiner's Comment Regarding Requirement for Deposit of Biological Material

- ☐ Notice of Informal Patent Application (PTO-152)
- ☐ Interview Summary (PTO-413), Paper No./Mail Date \_\_\_\_\_
- ☐ Examiner's Amendment/Comment
- ☒ Examiner's Statement of Reasons for Allowance
- ☐ Other \_\_\_\_\_

*Jean Bruner Jeanglaude*  
Jean Bruner Jeanglaude  
Primary Examiner

Allowable Subject Matter

Claims 1 – 43 are allowable.

1. The following is an examiner's statement of reasons for allowance: in combination with other limitations of the claims the prior arts made of record fail to suggest a system and process for transmitting at given instants digital signals on a bus, the process detects the digital signals as a condition in which transmission on the bus in a non-encoded format and an encoded format are able to give rise to an identical switching activity on the bus and determines whether a digital signal to be transmitted on the bus at a given instant is to be transmitted in the non-encoded format or in the at least partially encoded format so as to cause the additional signal associated to the signal to be transmitted on the bus at a given instant to keep a logic value with respect to a logic value assumed by the additional signal for transmission on the bus for a preceding instant among the given instants. Also, in combination with other limitations of the claims the prior arts made of record fail to suggest a decoder for receiving digital signals transmitted on a bus comprising a decoding logic to identify for each digital signal received, at least one marker bit that separates a first set of bits in a non-encoded format from a second set of bits in an encoded format and a logic construction network for reconvertng the encoded format to the non-encoded format the bits of the second set. Moreover, in combination with other limitations of the claims the prior arts made of record fail to suggest a method of transmitting a signal on a bus in which is a first signal activity value is greater than a second signal activity value, transmitting the signal in a second format; if the first signal activity value is less than the second signal

activity value, transmitting the signal in a first format and; if the first signal activity value is equal to the second signal activity value, compares a third signal activity value associated with transmitting the signal in a first format with a fourth activity value associated with transmitting the signal in a second format and if the third signal activity value is greater than the fourth signal activity value transmitting the signal in the second format and if the third signal activity value is less than the fourth signal activity value, transmitting the signal in the first format. Furthermore, in combination with other limitations of the claims, the prior arts made of record fail to suggest a transmitter for transmitting a signal on a bus, comprising a module for calculating switching activity coupled to the receiving module and to the formatting module, for calculating first and second switching activity values associated with transmitting the received signal and third and fourth switching activity values associated with transmitting the formatted received signal; a comparator coupled to the module for calculating switching activity for comparing the first and third switching activity values and the second and fourth switching activity values; and a driver coupled to the comparator, the receiving module and the formatting module, for driving transmission of one of the received signal and the formatted received signal, based, at least in part, on the comparisons of the first and third switching activity values and the second and fourth switching activity values.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

### Conclusion

2. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.
3. Ariyavisitaku et al. (US Patent Number 5,084,891) discloses a technique for jointly performing bit synchronization and error detection in a TDM/TDMA system.
4. Bain (US Patent Number 5,651,015) discloses an apparatus and method for synchronization and error detection of received digital data bursts in a TDM/TDMA system.
5. Hoang et al. (US Patent No. 5,719,632) discloses a motion video compressed system with buffer empty/fill look-ahead bit allocation.
6. Burd (US Patent Number 6,504,493) discloses a method and apparatus for encoding/decoding data.
7. Aziz (US Patent number 6,606,728) discloses a rate code encoder, detector, and decoder for control data.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Jean B. Jeanglaude whose telephone number is 571-272-1804. The examiner can normally be reached on Monday - Friday 7:30 A. M. - 5:00 P.M..

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Michael Tokar can be reached on 571-272-1812. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).



Jean Bruner Jeanglaude  
Primary Examiner  
April 26, 2005